



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2811

Examiner: Ori Nadav

In Re PATENT APPLICATION of:

Applicant(s): Hiroaki NAKANO

Serial No.: 10/756,366

Filing date: January 14, 2004

For: CIRCUIT BOARD FOR MOUNTING A  
SEMICONDUCTOR CHIP AND  
MANUFACTURING METHOD THEREOF

Atty. ref.: TAI 146

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) **AMENDMENT**  
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April 13, 2006

**Mail Stop AF**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

In response to the Examiner's Action mailed on January 23, 2006, please  
amend the above-identified application as follows: